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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 14498/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date Submitted: February 7, 2005

(use as many sheets as necessary)

Sheet 1 of 2

## Complete if Known

Application Number	10/620,194
Filing Date	07/15/2003
First Named Inventor	Eric N. Paton
Group Art Unit	2823
Examiner Name	Joannie A. Garcia
Attorney Docket Number	039153-0646 (H0975)

## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
JAG	A1	2004/0089914	A1	Mouli et al.	05-13-2004	
JAG	A2	2004/0061161	A1	Radens et al.	04-01-2004	
JAG	A3	8,673,696	B1	Arasnia et al.	01-06-2004	
JAG	A4	2003/0139051	A1	Andideh et al.	07-24-2003	
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JAG	A6	6,524,931	B1	Perera	02-25-2003	
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JAG	A14	5,266,813		Comfort et al.	11-30-1993	

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
JAG	A15	WO	02/101818	A2	Currie et al.	12/19/2002		
JAG	A16	WO	02/095818	A1	Beyer et al.	11/28/2002		
JAG	A17	GB	2 254 731	A	Samsung Electronics Company Limited	10-14-1992		
JAG	A18	EP	0 245 622	B1	International Business Machines Corporation	11-19-1987		

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
JAG	A19	Written Opinion of the International Searching Authority; International Application No. PCT/US2004/007464; International Filing Date 03-11-2004; 6 pgs.	

Examiner Signature

Joannie A. Garcia

Date Considered

April 13, 2005

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. \*See attached Kinds of U.S. Patent Documents. \*Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). \*For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>2</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \*Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: February 7, 2005 (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/620,194
		Filing Date	07/15/2003
		First Named Inventor	Eric N. Paton
		Group Art Unit	2823
		Examiner Name	Joannie A. Garcia
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		Attorney Docket Number	039153-0646 (H0975)

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
JAG	A20	International Search Report; International Application No. PCT/US2004/007464; mailed 01/09/2004; 3 pgs.	
JAG	A21	OHKUBO, Satoshi; TAMURA Yasuyuki; SUGINO, Rinji; NAKANISHI, Toshiro; SUGITA, Yoshihiro; AWAJI, Naoki and TAKASAKI, Kanetake; "High Quality Ultra-Thin (4nm) Gate Oxide by UV/O <sub>2</sub> Surface Pre-Treatment of Native Oxide"; 1995 Symposium On VLSI Technology; Digest of Technical Papers; June 6-8, 1995; Kyoto, Japan; 3 pgs.	
JAG	A22	Written Opinion of the International Searching Authority; International Application No. PCT/US2004/000982; International Filing Date 01-13-2004; 6 pgs.	
JAG	A23	International Search Report for PCT/US2004/000982, 4 pages.	
JAG	A24	Wolf, Stanley, Ph.D. and Tauber, Richard N., Ph.D.; "Silicon Processing For The VLSI Era"; Vol. 1: Process Technology; Lattice Press; 1986; pp. 57-58, 194	
JAG	A25	Ghandhi, Sorab K. "VLSI Fabrication Principles"; John Wiley and Sons; 1983; pp. 422-423	
JAG	A26	VAN ZANT, Peter, "Microchip Fabrication", McGraw Hill, 1977, pages 31 and 39	
JAG	A27	VOSSSEN, John L. and KERN, Werner, "Thin Film Processes II, Academic Press, 1991, page 333	
JAG	A28	RIM, K., WELSER, J., HOYT, J.L., and GIBBONS, J.F., "Enhanced Hole Mobilities in Surface-channel Strained-Si p-MOSFETs", 1995 International Electron Devices Meeting TECHNICAL DIGEST, 5 pages.	
JAG	A29	WELSER, J., HOYT, J.L., TAKAGI, S. and GIBBONS, J.F., "Strain Dependence of the Performance Enhancement in Strained-Si n-MOSFETs", 1994 International Electron Devices Meeting TECHNICAL DIGEST, San Francisco, CA December 11-14, 1994, 5 pages.	

Examiner Signature	<i>Joannie A. Garcia</i>	Date Considered	<i>April 13 2005</i>
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